



ALPIBLOC - Fixed power factor correction

Part number: B17540



Alpibloc is an ALPIAVR3 capacitor with built-in circuit breaker, assembly fitted and wired in an IP31 – IK05 box or cabinet. Equipment supplied ready for connection, for fixed PFC.

Contents	Page
1. Technical data	1
2. Description	1
3. Additional equipment	1
4. Installation instructions	1

1. Technical data

Range:	ALPIBLOC
Type:	Standard
Nominal power:	175 kVAr (75 + 100 kVAr fixe)
Nominal voltage:	400 V - 50 Hz - Tri
Max permissible voltage:	470 V (capacitors)
Harmonic level:	THDU < 2% et THDI < 5% SH/ST ≤ 15%
Insulation class:	6 kV / 25 kV
Loss factor:	2 W / kVAr
Temperature class:	-10°C / +45°C Average over 24H: +40°C Annual average: +35°C
Weight:	125 kg
Dimensions (W x D x H mm) :	520 x 320 x 770
Type :	Indoor
Storage :	-30°C / +60°C
Environment :	Dry Dust-free Non-corrosive Vibration-free
Standard:	IEC 60831-1 and 2

3. Additional equipment

Power connecting cables with minimum cross section of 120 mm² Cu / phase.

4. Installation instructions

Wait for 3 minutes before reconnecting the capacitor in compliance with mandatory discharging time.

External environment for the cubicle:

- Dry and dust free
- Non-corrosive atmosphere
- Maximum temperature: +40°C
- Average over 24 hours: +35°C
- Annual average: +25°C

A minimum clearance distance of 200 mm must be respected against any obstacle (wall, distribution board) to insure good ventilation.

For more details on the commissioning and maintenance, refer to installation instructions.

2. Description

This fixed capacitor bank includes:

- **2 ALPIVAR3 capacitors:**
 - Totally dry unit (without oil impregnation)
 - Double insulation or class II
 - Internal electrical protection by self-healing film, electrical fuse and disconnection device in case of overpressure
 - With discharge resistors (discharging time 3 minutes)
 - Complies with international standard (IEC 60831-1 & 2)
- **1 circuit breaker 400 A, thermal setting at 400 A, I_{sc}=36 kA/400 V.**
- **1 cubicle, IP31-IK05, in which are mounted and wired the above components.**